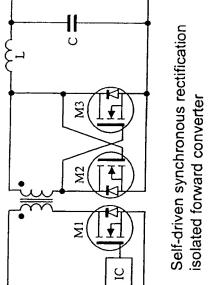


IC-driven synchronous rectification isolated forward converter





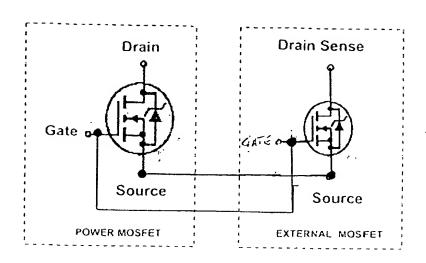
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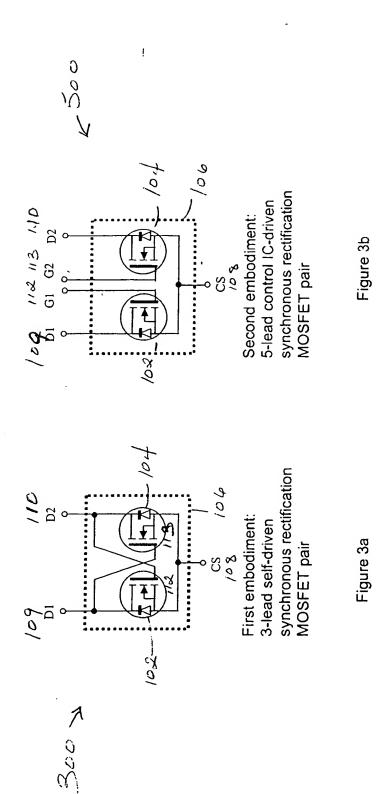


Figure 3

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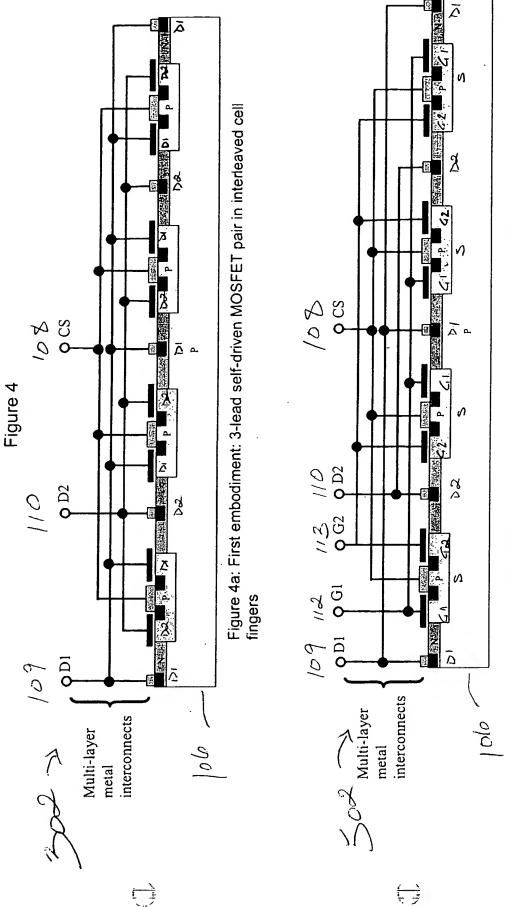
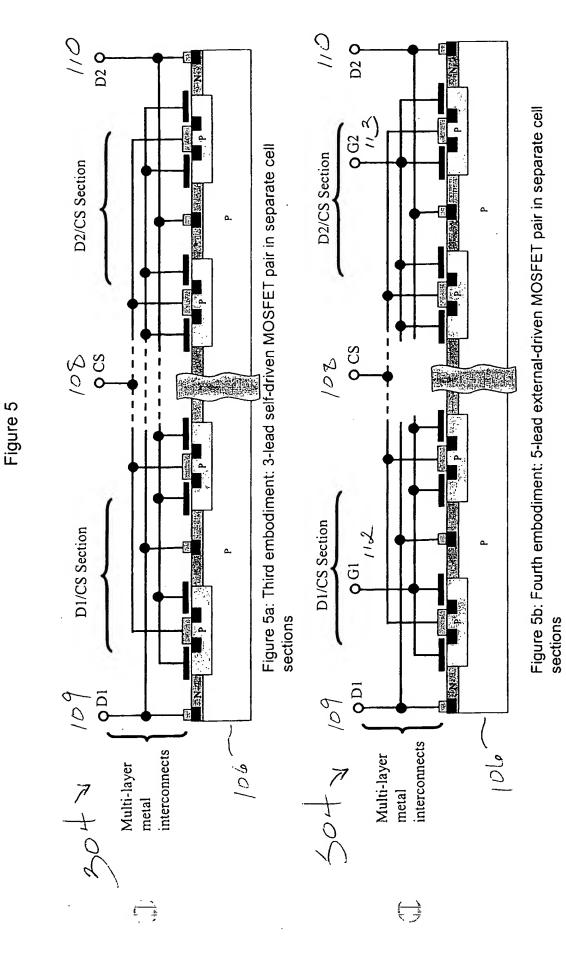


Figure 4b: Second embodiment: 5-lead external-driven MOSFET pair in interleaved cell fingers

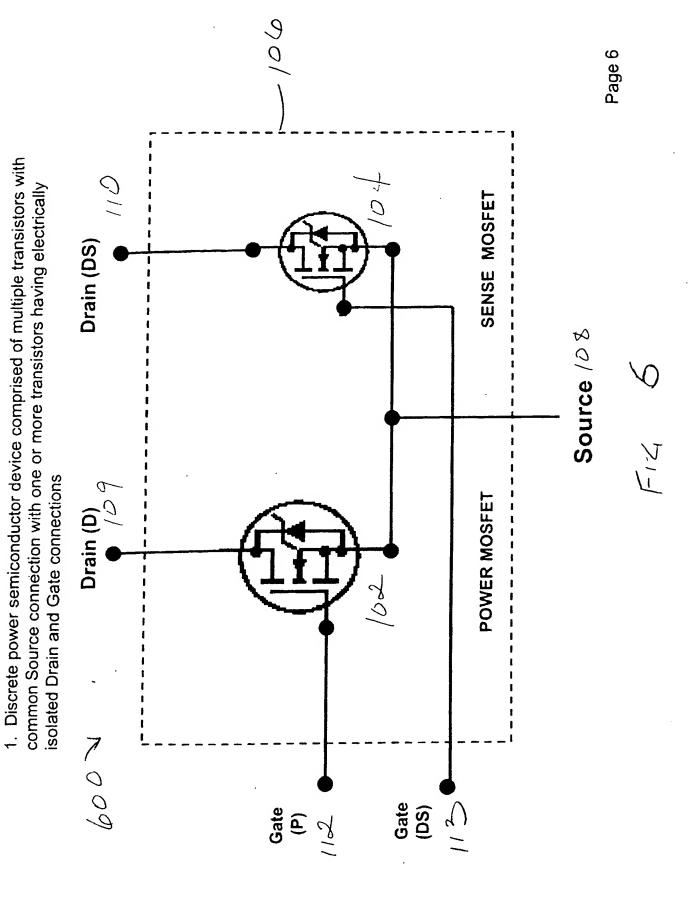
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1. Discrete power semiconductor device comprised of multiple transistors with common Source connection with one or more transistors having electrically isolated Drain and Gate connections

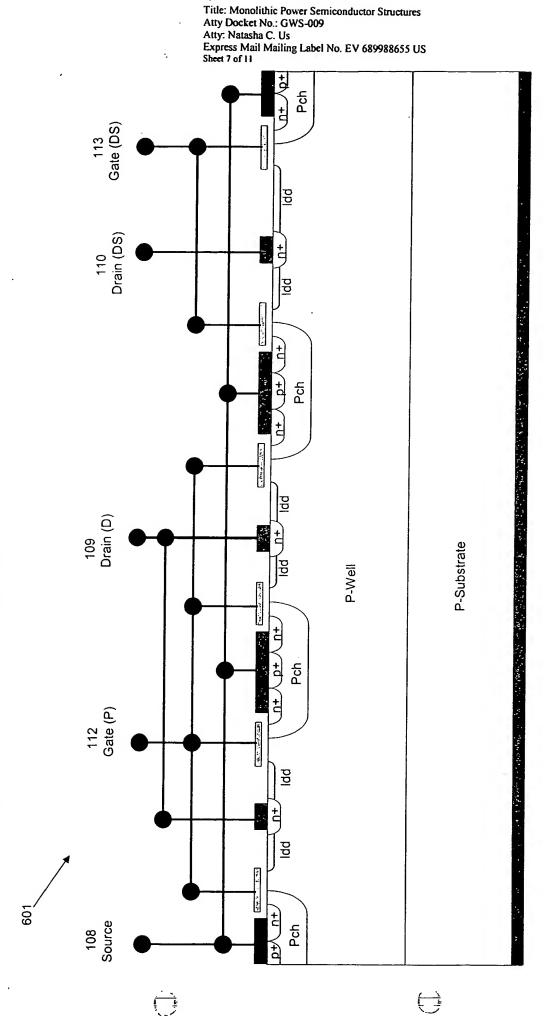


Figure 7

Cross-Sectional Diagram of a Power MOSFET With Integrated Drain Sense

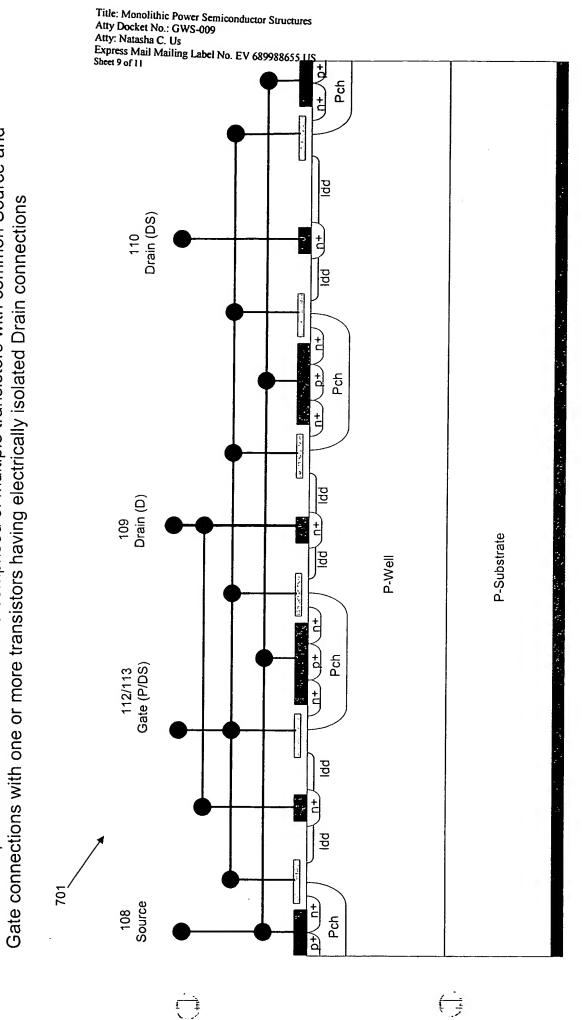
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Source

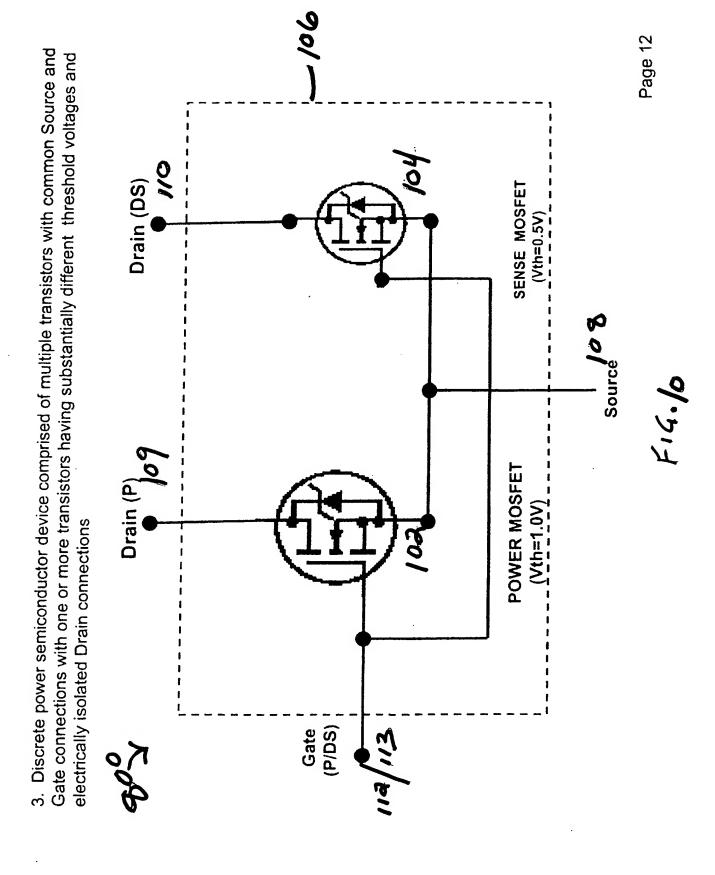
000 2. Discrete power semiconductor device comprised of multiple transistors with common Source and Gate connections with one or more transistors having electrically isolated Drain connections SENSE MOSFET Drain (DS) POWER MOSFET Gate (P/DS)

2. Discrete power semiconductor device comprised of multiple transistors with common Source and



Cross-Sectional Diagram of a Power MOSFET With Integrated Drain Sense Figure 9

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Title: Monolithic Power Semiconductor Structures Atty Docket No.: GWS-009 Atty: Natasha C. Us Express Mail Mailing Label No. EV 689988655 US --- Indicates Threshold Adjust Implant Pch 3. Discrete power semiconductor device comprised of multiple transistors with common Source and Gate connections with one or more transistors having substantially different threshold voltages and Drain (DS) A.A. Sterney t Pch ŧ) 109 Drain (P) P-Substrate pp P-Well electrically isolated Drain connections +d / +u P G 112/113 Gate (P/DS) <u>원</u> pp 801 Source +u /+a 108 Pch 

Cross-Sectional Diagram of a Power MOSFET With Integrated Drain Sense

Figure 11